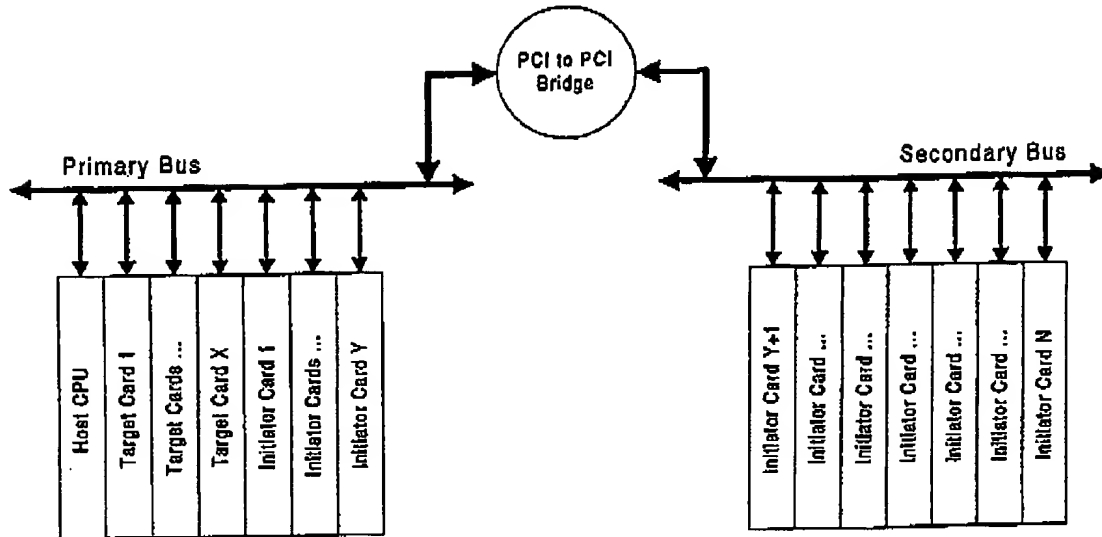
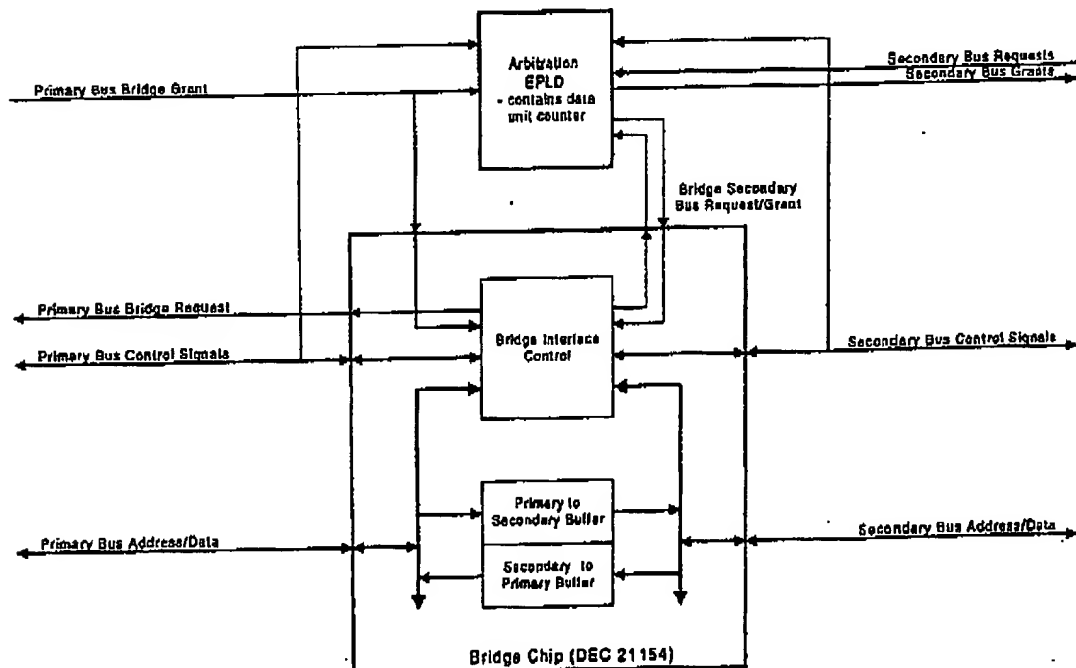


**Exhibit "B"**

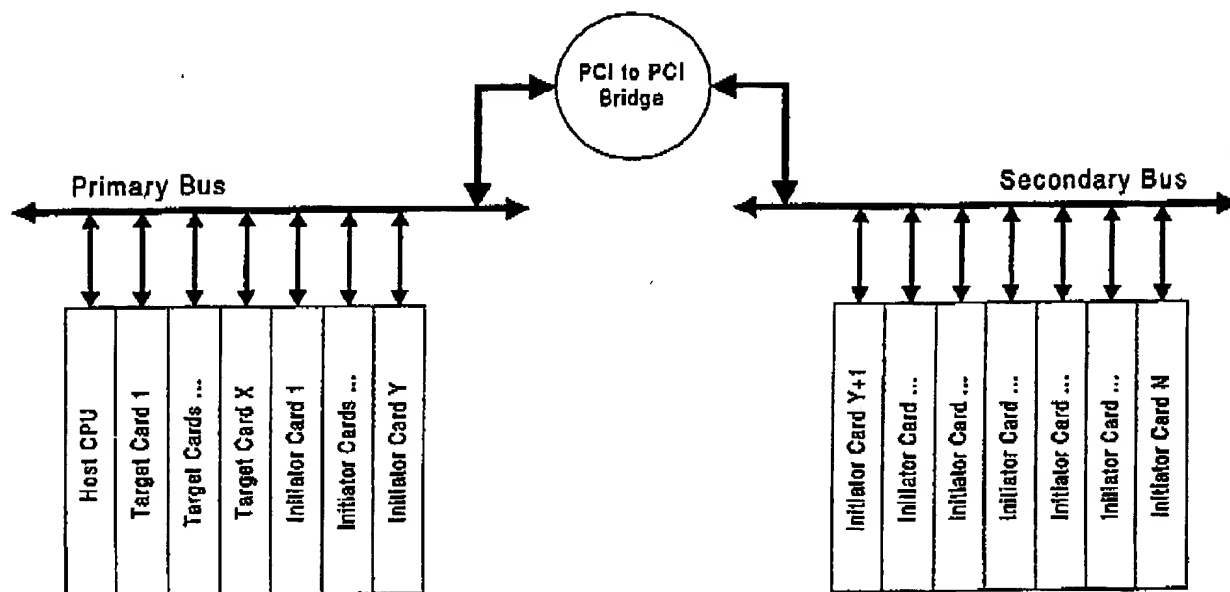
BWA Compact PCI Architecture with Host CPU, X Target Cards and N Initiator Cards:



Architecture of PCI to PCI Bridge module using the method described in the invention disclosure form:



**Exhibit "B"**

**BWA Compact PCI Architecture with Host CPU, X Target Cards and N Initiator Cards:****Architecture of PCI to PCI Bridge module using the method described in the invention disclosure form:**